

STPS40120C

Power Schottky rectifier

Main product characteristics

I _{F(AV)}	2 x 20 A
V _{RRM}	120 V
T _{j(max)}	175° C
V _{F(typ)}	0.57 V

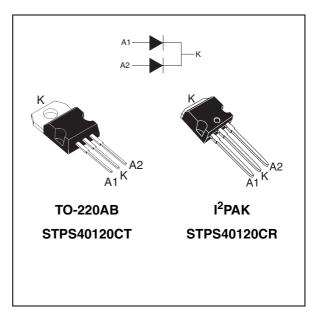
Feature and benefits

- High junction temperature capability
- Avalanche rated
- Low leakage current
- Good trade-off between leakage current and forward voltage drop

Description

Dual center tap Schottky rectifier suited for high frequency Switch Mode Power Supply.

Packaged in TO-220AB and I²PAK, this device is intended to be used in notebook and LCD adaptors, desktop SMPS, providing in these applications a margin between the remaining voltages applied on the diode and the voltage capability of the diode.



Order code

Part Number	Marking
STPS40120CT	STPS40120CT
STPS40120CR	STPS40120CR

Symbol		Parameter				
V _{RRM}	Repetitive peak reverse voltag	je		120	V	
I _{F(RMS)}	RMS forward voltage			30	А	
I _{F(AV)}	Average forward current	20 40	A			
I _{FSM}	Surge non repetitive forward c	Surge non repetitive forward current t _p = 10 ms Sinusoidal				
P _{ARM}	Repetitive peak avalanche pov	10500	W			
T _{stg}	Storage temperature range	-65 to + 175	°C			
Тj	Maximum operating junction te	Maximum operating junction temperature ⁽¹⁾				

Table 1.	Absolute ratings (limiting values, per diode)

1. $\frac{dPtot}{dT_i} < \frac{1}{Rth(i-a)}$ condition to avoid runaway for a diode on its own heatsink

Characteristics 1

Table 2.	Thermal	parameters
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Symbol	Parame	eter	Value	Unit
R _{th(j-c)}	Junction to case	Per diode Total	1.6 0.85	° C/W
R _{th(c)}	Coupling	Total	0.1	° C/W

When the diodes 1 and 2 are used simultaneously :

 $\Delta T_{i}(\text{diode 1}) = P(\text{diode 1}) \times R_{th(j-c)}(\text{per diode}) + P(\text{diode 2}) \times R_{th(c)}$

Table 3.	Static electrical	characteristics	(per diode))
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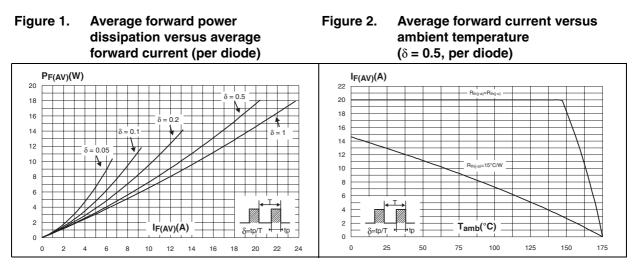
Symbol	Test conditions			Min.	Тур.	Max.	Unit
I _B ⁽¹⁾	Reverse leakage current	$T_j = 25^\circ C$	V _R = V _{RRM}			25	μA
'R`	neverse leakage current	T _j = 125° C	VR = VRRM		4	12	mA
			I _F = 7.5 A			0.73	
	V _F ⁽²⁾ Forward voltage drop	$T_j = 25^\circ C$ $T_j = 125^\circ C$	ι _F – 7.3 Α		0.57	0.61	
V_(2)		$T_j = 25^{\circ} C$ $T_j = 125^{\circ} C$				0.9	V
v F. (T _j = 125° C	I _F = 20A		0.69	0.73	v
		$T_j = 25^{\circ} C$ $T_j = 125^{\circ} C$	I _F = 40 A			1	
		$T_j = 125^\circ C$	1F – 40 A		0.83	0.88	

1. Pulse test : tp = 5 ms, δ < 2%

2. Pulse test : tp = 380 μ s, δ < 2%

To evaluate the maximum conduction losses use the following equation : P = 0.58 x $I_{F(AV)}$ + 0.0075 ${I_F}^2_{(RMS)}$





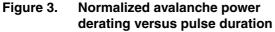


Figure 4. Normalized avalanche power derating versus junction temperature

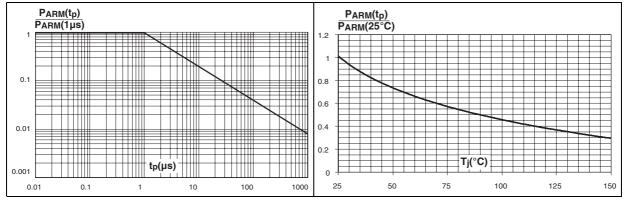
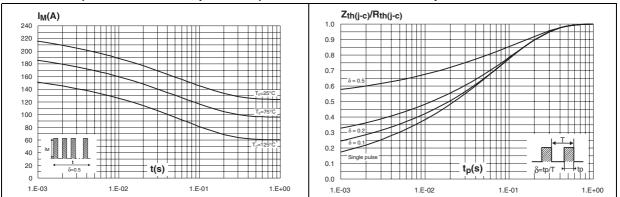


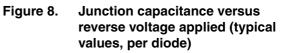
Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values, per diode)

Figure 6. Relative variation of thermal impedance junction to ambient versus pulse duration



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Figure 7. Reverse leakage current versus reverse voltage applied (typical values, per diode)



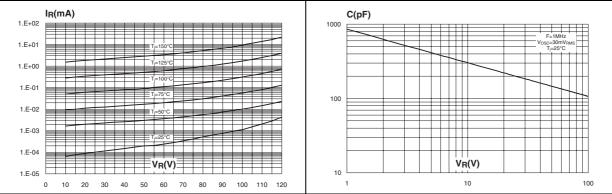
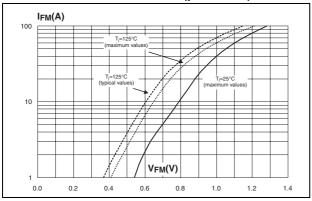


Figure 9. Forward voltage drop versus forward current (per diode)



2 Package information

- Epoxy meets UL94, V0
- Cooling method: by conduction (C)
- Recommended torque value: 0.8 Nm
- Maximum torque value: 1.0 Nm

Figure 10. TO-220AB dimensions

				DIMEN	SIONS	
		REF.	Millim	neters	Inches	
			Min.	Max.	Min.	Max.
		А	4.40	4.60	0.173	0.181
		С	1.23	1.32	0.048	0.051
	A →L	D	2.40	2.72	0.094	0.107
	+	Е	0.49	0.70	0.019	0.027
	L7	F	0.61	0.88	0.024	0.034
	√* □	F1	1.14	1.70	0.044	0.066
		F2	1.14	1.70	0.044	0.066
		G	4.95	5.15	0.194	0.202
	•	G1	2.40	2.70	0.094	0.106
F→ ←		H2	10	10.40	0.393	0.409
		L2	16.4	typ.	0.645	5 typ.
	E	L4	13	14	0.511	0.551
		L5	2.65	2.95	0.104	0.116
		L6	15.25	15.75	0.600	0.620
		L7	6.20	6.60	0.244	0.259
		L9	3.50	3.93	0.137	0.154
		М	2.6	typ.	0.102	2 typ.
		Diam.	3.75	3.85	0.147	0.151

Table 4. I FAR differen						
				Dimer	nsions	
		Ref.	Millimeters		Incl	hes
			Min.	Max.	Min.	Max.
	A	А	4.40	4.60	0.173	0.181
│	<u>c2</u>	A1	2.40	2.72	0.094	0.107
L2		b	0.61	0.88	0.024	0.035
		b1	1.14	1.70	0.044	0.067
	D	С	0.49	0.70	0.019	0.028
		c2	1.23	1.32	0.048	0.052
	A1	D	8.95	9.35	0.352	0.368
		е	2.40	2.70	0.094	0.106
		e1	4.95	5.15	0.195	0.203
		E	10	10.40	0.394	0.409
	_→ <mark>⊂ C</mark>	L	13	14	0.512	0.551
e ⊓ € 1		L1	3.50	3.93	0.138	0.155
		L2	1.27	1.40	0.050	0.055

Table 4.I²PAK dimensions

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.



3 Ordering information

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
STPS40120CT	STPS40120CT	TO-220AB	2.23 g	50	Tube
STPS40120CR	STPS40120CR	I ² PAK	1.49 g	50	Tube

4 Revision history

Date	Revision	Description of Changes
18-Feb-2005	1	First issue
1-Dec-2006	2	Reformatted to current standards. Added I ² PAK.



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